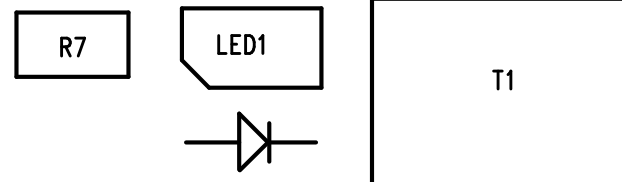



TOP SILKSCREEN
 LINEAR TECHNOLOGY DATE: 09-05-12
 DC1979A-2 LTC6090CS8E
 HIGH-VOLTAGE CMOS AMPLIFIER

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	JON M.	09-05-12

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER BOTH SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL LED1 AS SHOWN BELOW



APPROVALS		 <div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY </div>		
PCB DES.	AK			
APP ENG.	JON M.	TITLE: TOP ASSEMBLY DRAWING		
		HIGH-VOLTAGE CMOS AMPLIFIER		
		SIZE	IC NO.	REV.
		N/A	LTC6090CS8E DEMO CIRCUIT 1979A	2
SCALE = NONE		FILENAME: DC1979A-2.PCB		SHT 1 OF 2